

❖ Features



- Single Pulsed RAS-interface
- Fully Synchronous to positive CLK edge
- 4 banks controlled by BA0 & BA1
- Multiple Burst Read with single write option
- Automatic and controlled pre-charge command
- Intend for 133 MHz applications
- Auto refresh (CBR) and Self-Refresh
- Inputs and Outputs are LVTTTL compatible
- VDD=VDDQ= 3.3 Volt ± 0.3
- 30u" gold fingers

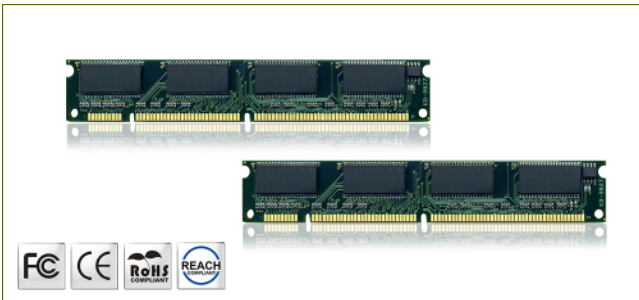
❖ Specification

SDRAM SO-DIMM Standard grade	SDRAM SO-DIMM Industrial grade
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- **Interface** JEDEC Standard 144-pin Dual In-Line Memory Module
- **IC Data Rate** SDR-133
- **Bandwidth** 64 bits
- **DIMM Data Rate** PC133 / PC100
- **Capacity** 128MB / 256MB
- **Pin Number** 144 Pins
- **Height** 30 mm + 0.15 mm
- **Voltage** 3.3V ± 0.3V
- **Warranty** 5 years

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❖ Specification

SDRAM U-DIMM Standard grade	SDRAM U-DIMM Industrial grade
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- **Interface** JEDEC Standard 168-pin Dual In-Line Memory Module
- **IC Data Rate** SDR-133
- **Bandwidth** 64 bits
- **DIMM Data Rate** PC133 / PC100
- **Capacity** 128MB / 256MB
- **Pin Number** 168 Pins
- **Height** 21.90 mm + 0.15 mm
- **Voltage** 3.3V ± 0.3V
- **Warranty** 5 years

- **Interface** JEDEC Standard 168-pin Dual In-Line Memory Module
- **IC Data Rate** SDR-133
- **Bandwidth** 64 bits
- **DIMM Data Rate** PC133 / PC100
- **Capacity** 128MB / 256MB
- **Pin Number** 168 Pins
- **Height** 21.90 mm + 0.15 mm
- **Voltage** 3.3V ± 0.3V
- **Warranty** 5 years

SLC

oSLC

MLC

3D-NAND

DRAM Module

Adapter

Card-drive