

3D NAND Flash

(TOSHIBA BICS FLASH[™])

SATA III CFastTM Card

PHANES-K Series

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ISO 9001 : 2015 CERTIFIED





Product Features

Flash IC

- TOSHIBA BICS FLASH[™].*³
- 3D NAND Flash

Compatibility

- Compliant with SATA Revision 3.2
- Compliant with CFast[™] Specification 2.0
- SATA 1.5Gb/s; SATA 3Gb/s & SATA 6Gb/s
- Interface compatible.
- ATA-8 ACS4 command set

Additional Capabilities

- S.M.A.R.T.^{*1} (Self-Monitoring, Analysis and Reporting Technology) feature set support.
- Native Command Queuing (NCQ) support.
- TRIM maintenance command support.
- Both Static & Dynamic wear-leveling algorithm
- Hardware Low Density Parity Check Code, LDPC support.
- Support bad Block Management
- Support DIPM/HIPM Mode for power saving

Mechanical

- 7-pin (data) + 17-pin (power) SATA III CFast[™] Card connector
- Dimension: 42.8 mm x 36.4 mm x 3.5 mm.
- Weight:
- Plastic frame-kit: 10g / 0.35 oz.
- Metal frame-kit: 13g / 0.46 oz.

■ Power Operating Voltage 3.3V(+/-) 5%

- Read Mode: 1,270.0 mW (max.)
- Write Mode: 1,360.0 mW (max.)
- Idle Mode: 325.0 mW (max.)

Performance (Maximum value) *²

- Sequential Read: 550.0 MB/sec. (max.)
- Sequential Write: 450.0 MB/sec. (max.)

Capacity

- 64GB and 128GB

Reliability

- **TBW:** Up to 75.0 TBW at 128GB Capacity. (Client workload by JESD-219A)
- ECC: Designed with hardware LDPC ECC engine with hard-decision and soft-decision decoding.
- Temperature: (Operating)
 Standard Grade: 0°C ~ +70°C
 Wide Temp. Grade: -40°C ~ +85°C
- Vibration: 80 Hz to 2000 Hz, 20G, 3 axes
- Shock: 0.5ms, 1500 G, 3 axes

Certifications and Declarations

- Certifications: CE & FCC
- Declarations: RoHS & REACH

Remarks:

- 1. Support official S.M.A.R.T. Utility.
- Sequential performance is based on CrystalDiskMark
 5.1.2 with file size 1000MB
- BiCS means Bit Cost Scalable Technology.
 BiCS FLASH is a trademark of Toshiba Corporation.



Order Information

I. Part Number List

◆ APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series with plastic frame kit

Product Picture	Grade	Standard grade (0°C ~ 70°C)	Wide Temp. Grade(-40°C ~ +85°C)
5000G	64GB	SPCFA064G-PKCT3	WPCFA064G-PKCT3-C
	128GB	SPCFA128G-PKCT3	WPCFA128G-PKCT3-C

APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series with rugged metal frame kit

Product Picture	Grade	Standard grade (0°C ~ 70°C)	Wide Temp Grade(-40°C ~ +85°C)
.°0000	64GB	SRCFA064G- PKCT3	WRCFA064G- PKCT3-C
GDDO ,	128GB	SRCFA128G- PKCT3	WRCFA128G- PKCT3-C
INDUSTRIAL			

Notes:

C: Special conformal coating treated on whole PCBA (Optional)

II. Part Number Decoder:

X1 X2 X3 X4 X5 X6 X7 X8 X9-X11 X12 X13 X14 X15 - X17

X1 : Grade		X12 : Controlle
S: Standard	Grade – operating temp. 0° C \sim 70 ° C	A, B, C
W: Wide Te	mp. Grade – operating temp40° C \sim +85 ° C	
		X13 : Controlle
X2 : The m	naterial of case	C : Commercial g
P: Plastic fr	ame kit	
R : Rugged	Metal frame kit	X14 : Flash IC
		T : Toshiba NAND
X3 X4 X5	Product category	
CFA : CFast	™ Card	X15 : Flash IC g
		3 : BICS 3D-NAN
X6 X7 X8	X9 : Capacity	
064G:	64GB	X17 : Reserved
128G:	128GB	C : Conformal coa
X11 : Cont	roller	

P: PHANES Series

er version

er Grade grade

D Flash IC

grade / Type

ND Flash IC.

d for specific requirement oating (optional)



Revision History

Revision	Description	Date
1.0	Initial release.	2018/11/26
2.0	Updated document form	2019/05/27

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1. Introduction

APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series provides high capacity flash memory Solid State Drive (SSD) that electrically complies with SATA Revision 3.2. APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series support SATA 1.5Gb/s; SATA 3Gb/s & SATA 6Gb/s data transfer rate with high performance. The main used flash memories are BiCS 3D NAND FLASH memory chips. The available disk capacities are 64GB and 128GB.

APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series also supports Metal Frame Kit as an optional product which may endure various harsh operating environments. The operating temperature grade is optional for standard grade 0°C ~ 70°C and Wide Temp. Grade -40°C ~ +85°C. The data transfer performance by sequential read is up to 550.0 MB/sec, and sequential write is up to 450.0 MB/sec.

APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series is suitable to handheld device embedded system, inventory recorder and particularly for serious environment monitor recorder system. Furthermore, APRO also provide 1.8" SATA to CFast[™] card Adapter (P/N: **AD-CA128SATA200AR**) to increase the application flexibility.

APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series provide a high level interface to the host computer. This interface allows a host computer to issue commands to the APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series to read or write blocks of memory. A powerful hardware design is architecture multiplied LDPC (Low Density Parity Check) for Error Correcting Coding (ECC). APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series intelligent controller manages interface protocols, data storage and retrieval as well as ECC, bad block management and diagnostics, power management and clock control.

Figure 1 shows a block diagram of the APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series.

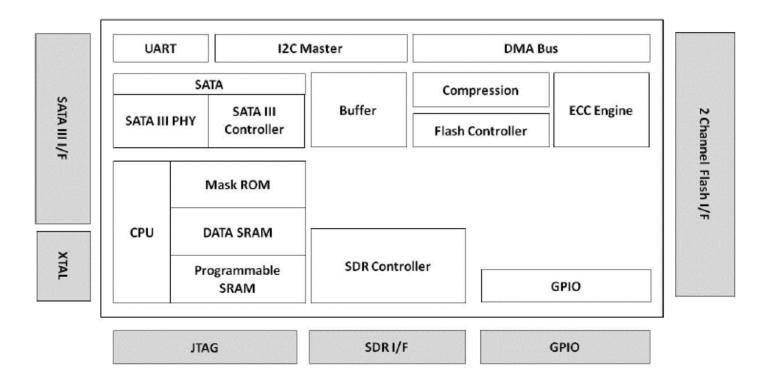


Figure 1: APRO SATA III CFastTM Card (3D NAND FLASH) PHANES-K Series block diagram

1.1. Scope

This document describes features, specifications and installation guide of APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series. In the appendix, there provides order information, warranty policy, RMA/DOA procedure for the most convenient reference.

1.2. Flash Management Technology – Static & Dynamic Wear Leveling

NAND flash devices can only undergo a limited number of program/erase cycles, and in most cases, the flash media are not used evenly. If some areas get updated more frequently than others, the lifetime of the device would be reduced significantly. Thus, Wear Leveling is applied to extend the lifespan of NAND Flash by evenly distributing write and erase cycles across the media.

APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series provides advanced Wear Leveling algorithm, which can efficiently spread out the flash usage through the whole flash media area. Moreover, by implementing both dynamic and static Wear Leveling algorithms, the life expectancy of the NAND flash is greatly improved.

1.3. Bad Block Management

Early Bad Block

The fault block generated during the manufacturing process of NAND Flash is called Early Bad Block.

Later Bad Block

In the process of use, as the number of operations of writing and erasing increases, a fault block is gradually generated, which is called a Latter Bad Block.

Bad block management is a management mechanism for a bad block to be detected by the control IC and mark bad blocks in the NAND Flash and improve the reliability of data access. The bad block management mechanism of the control IC will establish a **Bad Block Table** when the NAND Flash is started for the first time, and will also record the errors found in the process of use in the bad block table, and data is ported to new valid blocks to avoid data loss.

In order to detect the initial bad blocks to handle run time bad blocks, APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series provides the **Bad Block Management** scheme. It remaps a bad block to one of the reserved blocks so that the data contained in one bad block is not lost and new data writes on a bad block is avoided.

1.4. Error Correcting Coding (ECC)

APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series provides a high level interface to the host computer. This interface allows a host computer to issue commands to the APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series to read or write blocks of memory. A powerful hardware design is architecture multiplied LDPC (Low Density Parity Check) for Error Correcting Coding (ECC). APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series intelligent controller manages interface protocols, data storage and retrieval as well as ECC, bad block management and diagnostics, power management and clock control.

1.5. 3D-NAND Flash

3D NAND is a vertical implementation of the NAND flash cell memory array. The memory cell transistors forming the NAND string are connected in a series vertically and the memory transistors are changed from the floating-gate type to a trapped charge type. In floating-gate technology, die density is increased by shrinking peripheral circuits and active circuits.

With 3D, holding the X/Y dimension of the die constant, die density is increased through multiple layers of the active circuits on the Z axis. Higher-density 3D NAND die enables applications needing high-density NAND chip solutions.

2. Product Specifications

For all the following specifications, values are defined at ambient temperature and nominal supply voltage unless otherwise stated.

2.1. System Environmental Specifications

Table 1. Environmental opecification			
APRO SATA III CFast [™] Card PHANES-K Series		Standard Grade	Wide Temp. Grade
(3D NAND FLASH)		SPCFAxxxG-PKCT3	WPCFAxxxG-PKCT3-C
T	Operating:	0°C ~ +70°C	-40°C ~ +85°C
Temperature	Non-operating:	-20°C ~ +80°C	-50°C ~ +95°C
Humidity Operating & Non-operating:		10% ~ 95% non-condensing	
Vibration Frequency/Acceleration:		80 Hz to 2000 Hz, 20G, 3 axes	
Shock Operating & Non-operating:		0.5ms, 1500 G, 3 axes	
Temperature:		24°C	
Electrostatic Relative Humidity:		49% (RH)	
Discharge (ESD)		Device functions are affected, but EUT will be back to its normal or	
	+/-4KV:	operational state automatically.	

Table 1: Environmental Specification

2.2. System Power Requirements

Table 2: Power Requirement

APRO SATA III CFast [™] Card (3D NAND FLASH) PHANES-K Series			
DC Input Voltage (VCC)		3.3V±5%	
Maximum average value	Reading Mode :	1,270.0 mW (max.)	
	Writing Mode :	1,360.0 mW (max.)	
	Idle Mode :	325.0 mW (max.)	

2.3. System Performance

Table 3: System Performances

Data Transfer Mode supporting		Serial ATA Gen-III (6.0Gb/s = 768MB/s)		
BA = 11 ² 1221122	Capacity	64GB	128GB	
Maximum	Sequential Read (MB/s)	550.0	550.0	
Performance	Sequential Write (MB/s)	255.0	450.0	

Note:

> The performance was measured using CrystalDiskMarkv5.0; 1GB data size test with SATA 6Gbps host.

> Samples were built using Toshiba BiCS 3D-NAND FLASH

> Performance may differ according to flash configuration and platform.

2.4. System Reliability

Table 4: System Reliability

Wear-leveling Algorithms		Static and Dynamic wear-leveling algorithms	
Bad Block Management Supportive		Supportive	
ECC Technolog	Hardware design LDPC (Low Density Parity Check)		
Erase counts		TOSHIBA BICS FLASH[™] 3D NAND Flash: 3K P/E Cycles	
TBW (Tera Bytes Written)			
64GB		42.0	
Capacity	128GB	75.0	

Note:

- Client workload by JESD-219A.
- > Samples were built using Toshiba BiCS 3D-NAND FLASH
- The endurance of SSD could be varying based on user behavior, NAND endurance cycles, and write amplification factor. It is not guaranteed by flash vendor.



2.5. Physical Specifications

Refer to Table 5 and see Figure 2 for APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series physical specifications and dimensions.

Table 5: Physical Specifications of APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series

Length:	42.8 mm	
Width:	36.4 mm	
Thickness:	3.5 mm	
	Plastic frame-kit: 10g (0.35 oz.)	
Weight:	Metal frame-kit: 13g (0.46 oz.)	

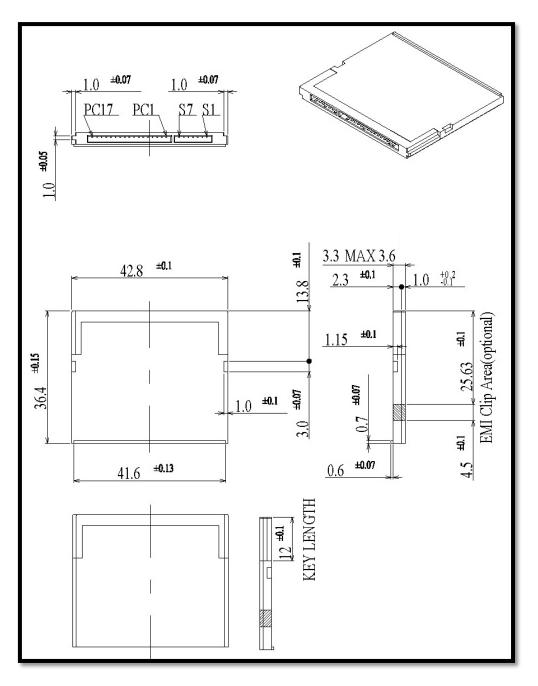


Figure 2: APRO SATA III CFastTM Card (3D NAND FLASH) PHANES-K Series Dimension

2.6. Conformal coating

Conformal coating is a protective, dielectric coating designed to conform to the surface of an assembled printed circuit board. Commonly used conformal coatings include silicone, acrylic, urethane and epoxy. APRO applies only silicone on APRO storages products upon requested especially by customers. The type of silicone coating features good thermal shock resistance due to flexibility. It is also easy to apply and repair.

Conformal coating offers protection of circuitry from moisture, fungus, dust and corrosion caused by extreme environments. It also prevents damage from those Flash storages handling during construction, installation and use, and reduces mechanical stress on components and protects from thermal shock. The greatest advantage of conformal coating is to allow greater component density due to increased dielectric strength between conductors. APRO use MIL-I-46058C silicon conformal coating

3. Interface Description

3.1. SATA III CFast[™] Card interface

APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series is equipped with 7 pins in the signal segment and 17 pins in the power segment.



Figure 3: The connectors of Signal Segment and Power Segment

3.2. Pin Assignments

APRO SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series operates with standard SATA pin-out.

The pin assignments are listed in below table 6.

Key and Spacing separate signal and power segments			
Name	Туре	Description	
S1	GND	NA	
S2	A+	Differential Signal Dair A	
\$3	A-	Differential Signal Pair A	
S4	GND	NA	
S5	В-	Differential Classed Data D	
S6	B+	Differential Signal Pair B	
S7	GND	NA	

P1	CDI	Card Detect In
P2	PGND	Device Ground
P3	DEVSLP	Device Sleep
P4	NA	Reserved
P5	NA	Reserved
P6	NA	Reserved
Р7	PGND	Device Ground
P8	LED1	LED Output
Р9	LED2	LED Output
P10	NA	Reserved
P11	NA	Reserved
P12	IFDET	NA
P13	PWR	Device Power
P14	PWR	Device Power
P15	PGND	Device Ground
P16	PGND	Device Ground
P17	CDO	Card Detect Out

Table 6 - Pin Assignments

Appendix A: Limited Warranty

APRO warrants your SATA III CFast[™] Card (3D NAND FLASH) PHANES-K Series against defects in material and workmanship for the life of the drive. The warranty is void in the case of misuse, accident, alteration, improper installation, misapplication or the result of unauthorized service or repair. The implied warranties of merchantability and fitness for a particular purpose, and all other warranties, expressed or implied, except as set forth in this warranty, shall not apply to the products delivered. In no event shall APRO be liable for any lost profits, lost savings or other incidental or consequential damages arising out of the use of, or inability to use, this product.

BEFORE RETURNING PRODUCT, A RETURN MATERIAL AUTHORIZATION (RMA) MUST BE OBTAINED FROM APRO.

Product shall be returned to APRO with shipping prepaid. If the product fails to conform based on customers' purchasing orders, APRO will reimburse customers for the transportation charges incurred.

WARRANTY PERIOD:

• 3D NAND FLASH (Standard grade / Wide Temp. Grade) 2 years / Within 3K Erasing Counts

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