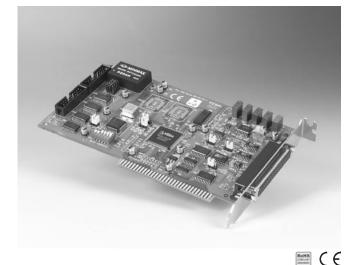
PCL-818HD/HG PCL-818L

100 kS/s, 12-bit, 16-ch ISA Multifunction Card

40 kS/s, 12-bit, 16-ch ISA Multifunction Card



Features

- 16 single-ended or 8 differential analog inputs
- 12-bit A/D converter, with up to 100 kHz sampling rate
- Programmable gain
- Automatic channel/gain scanning
- Onboard FIFO memory (1024 samples, PCL-818HD/HG only)
- One 12-bit analog output channel
- 16 digital inputs and 16 digital outputs
- Onboard programmable counter

Introduction

The PCL-818L series was designed for entry-level models to the PCL-818 series. The cards have been designed with the cost-sensitive customer in mind, but still offers the same functions as the rest of the series, except that they have a 40 kHz sampling rate and only accepts bipolar inputs. They are fully software and connector compatible with the PCL-818HD and PCL-818HG. This lets you upgrade your applications to these higher performance cards without hardware or software changes.

The PCL-818LS bundle consists of the PCL-818L card, the PCLD-8115 wiring terminal board and a DB37 cable assembly. The PCLD-8115 accommodates onboard passive signal conditioning components (resistors and capacitors), allowing you to easily implement a low-pass filter, a voltage attenuator or a 4 ~ 20 mA voltage converter.

Specifications

Analog Input

 Channels Resolution Max. Sampling Rate Overvoltage Protectio Input Impedance Sampling Modes 	12 bits 100 kS 40 kS/2 n ±30 V₀ 10 MΩ Softwa	G/s for all is for all ir ic max. 2 re, pacer	input rang nput range or externa	ges (PCL-{ es (PCL-8 al		G only)	- Cha - Cor - Out - Out Time
 Input Range 	(V, SO	ftware p	rogramn	nabie)			- Cha
Bipolar Accuracy (% of FSR ±1L	.SB)	±10 0.1	±5 0.1	±2.5 0.2	±1.25 0.2	±0.625 0.4	 A/D Max
Analog Output							- Ma
 Channels 	1						Gene
 Resolution 	12 bits	;					 Pov
 Output Rate 	Static ı	update					- 100
 Output Range 	(V, so	ftware p	rogramr	nable)			
Internal Reference	Unipola	r		0 ~ 5, 0	~ 10		= I/O
External Reference				0 ~ 10, 0	~ -10		
Digital Input							• Din • Ope
 Channels 	16						 Sto
 Compatibility 	5 V/TT	Ľ					• Ope
 Input Voltage 	0	0: 0.8 V m 1: 2.0 V m					

Digital Output

J	
Channels	16
Compatibility	5 V/TTL
Output Voltage	Logic 0: 0.4 V max. Logic 1: 2.4 V min.
Output Capability	Sink: 8 mA Source: -0.4 mA

Timer/Counter

	Channels	1
	A/D Pacer	32-bit with 10 MHz or 1 MHz time base
	Max. and Min. Rates	2.5 MHz to 0.00023 Hz
	Counter	One 16-bit counter with 100 kHz time base
G	eneral	
•	Power Consumption	+5 V @ 210 mA typical, 500 mA max. +12 V @ 20 mA typical, 100 mA max. -12 V @ 20 mA typical, 40 mA max.
	I/O Connector	DB37-F
		Box header 20P x 2
	Dimensions (L x H)	155 x 100 mm (6.1" x 3.9")
	Operating Temperature	0 ~ 50° C (32 ~ 122° F)
	Storage Temperature	-20 ~ 65° C (-4 ~ 149° F)
	Operating Humidity	5 ~ 95% RH, non-condensing (refer to IEC 68-2-3)

Ordering Information

 PCL-818HD PCL-818HG PCL-818L 	High-performance half-size multi-function card High-performance and high-gain multi-function card Low-cost high-performance half-size multi-function card
PCL-818LS	PCL-818L with PCLD-8115 and DB-37 cable assembly (PCL-10137-1)
PCL-10137-1	DB37 cable assembly, 1 m
PCL-10137-2	DB37 cable assembly, 2 m
PCL-10137-3	DB37 cable assembly, 3 m
PCLD-8115	Industrial Wiring Terminal with CJC circuit and DB37 connector
PCLD-880	Industrial Wiring Terminal with DB37 connector