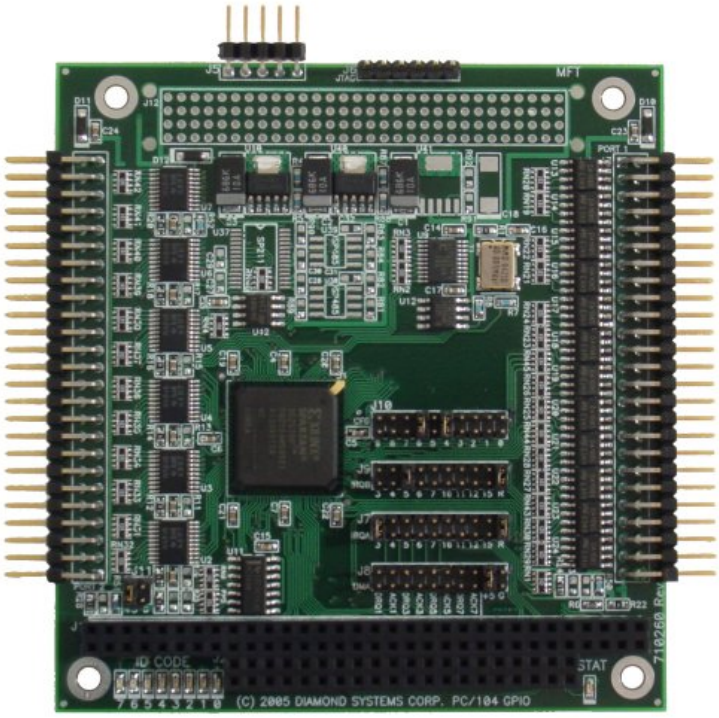
DIAMOND SYSTEMS CORPORATION

GPIO-MM

FPGA-based PC/104

Personality Guide V1.00



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1 GENERAL DESCRIPTION

The GPIO-MM contains a Xilinx Spartan-II FPGA and a reprogrammable configuration flash device. Using a JTAG cable, the configuration flash of the GPIO-MM can be reloaded with alternate personalities to change the functionality of the GPIO-MM.

Each official personality from Diamond Systems is given an 8-bit personality ID code. There are currently three personalities available: 0x11, 0x12 and 0x21.

1.1 Personality 0x11: Dual 9513 Counter/Timer Cores with 48 Buffered Digital I/O Lines (ACB)

This is the default personality loaded into the board during production. This personality is designed to be software and hardware compatible with the Quartz-MM and Garnet-MM.

Personality 0x11 emulates the Quartz-MM by providing two 9513 cores, for a total of ten 16-bit counter/timers. Each counter/timer has an input, output and gate pin, allowing for many different modes of operation. The Quartz-MM emulation header also provides 8 unbuffered digital input and 8 unbuffered digital output.

The buffered digital I/O is grouped into six ports of eight bits each. The digital I/O uses two 82C55 interfaces, same as the Garnet-MM. The pinout of the digital I/O is ordered “ACB” to be pinout compatible with the Garnet-MM.

This personality also provides 4 auxiliary bi-directional unbuffered digital I/O lines on J5.

J3: Counter/Timer Header

In 1	1	2	In 2
Gate 1	3	4	Gate 2
Out 1	5	6	Out 2
In 3	7	8	In 4
Gate 3	9	10	Gate 4
Out 3	11	12	Out 4
In 5	13	14	Out 5
Gate 5	15	16	FOUT
In 6	17	18	In 7
Gate 6	19	20	Gate 7
Out 6	21	22	Out 7
In 8	23	24	In 9
Gate 8	25	26	Gate 9
Out 8	27	28	Out 9
In 10	29	30	Out 10
Gate 10	31	32	Interrupt Input
Dout 7	33	34	Din 7
Dout 6	35	36	Din 6
Dout 5	37	38	Din 5
Dout 4	39	40	Din 4
Dout 3	41	42	Din 3
Dout 2	43	44	Din 2
Dout 1	45	46	Din 1
Dout 0	47	48	Din 0
+5V	49	50	Ground

J4: Buffered Digital I/O Header

DIOA 7	1	2	DIOD 7
DIOA 6	3	4	DIOD 6
DIOA 5	5	6	DIOD 5
DIOA 4	7	8	DIOD 4
DIOA 3	9	10	DIOD 3
DIOA 2	11	12	DIOD 2
DIOA 1	13	14	DIOD 1
DIOA 0	15	16	DIOD 0
DIOC 7	17	18	DIOF 7
DIOC 6	19	20	DIOF 6
DIOC 5	21	22	DIOF 5
DIOC 4	23	24	DIOF 4
DIOC 3	25	26	DIOF 3
DIOC 2	27	28	DIOF 2
DIOC 1	29	30	DIOF 1
DIOC 0	31	32	DIOF 0
DIOB 7	33	34	DIOE 7
DIOB 6	35	36	DIOE 6
DIOB 5	37	38	DIOE 5
DIOB 4	39	40	DIOE 4
DIOB 3	41	42	DIOE 3
DIOB 2	43	44	DIOE 2
DIOB 1	45	46	DIOE 1
DIOB 0	47	48	DIOE 0
+5V	49	50	Ground

1.2 Personality 0x12: Dual 9513 Counter/Timer Cores with 48 Buffered Digital I/O Lines (ABC)

This personality is identical to 0x11 (see above) in every way except for the arrangement of the buffered digital I/O on J4. The digital I/O in this personality is arranged "ABC" to be compatible with the Onyx-MM pinout.

J3: Counter/Timer Header

In 1	1	2	In 2
Gate 1	3	4	Gate 2
Out 1	5	6	Out 2
In 3	7	8	In 4
Gate 3	9	10	Gate 4
Out 3	11	12	Out 4
In 5	13	14	Out 5
Gate 5	15	16	FOUT
In 6	17	18	In 7
Gate 6	19	20	Gate 7
Out 6	21	22	Out 7
In 8	23	24	In 9
Gate 8	25	26	Gate 9
Out 8	27	28	Out 9
In 10	29	30	Out 10
Gate 10	31	32	Interrupt Input
Dout 7	33	34	Din 7
Dout 6	35	36	Din 6
Dout 5	37	38	Din 5
Dout 4	39	40	Din 4
Dout 3	41	42	Din 3
Dout 2	43	44	Din 2
Dout 1	45	46	Din 1
Dout 0	47	48	Din 0
+5V	49	50	Ground

J4: Buffered Digital I/O Header

DIOA 7	1	2	DIOD 7
DIOA 6	3	4	DIOD 6
DIOA 5	5	6	DIOD 5
DIOA 4	7	8	DIOD 4
DIOA 3	9	10	DIOD 3
DIOA 2	11	12	DIOD 2
DIOA 1	13	14	DIOD 1
DIOA 0	15	16	DIOD 0
DIOB 7	17	18	DIOE 7
DIOB 6	19	20	DIOE 6
DIOB 5	21	22	DIOE 5
DIOB 4	23	24	DIOE 4
DIOB 3	25	26	DIOE 3
DIOB 2	27	28	DIOE 2
DIOB 1	29	30	DIOE 1
DIOB 0	31	32	DIOE 0
DIOC 7	33	34	DIOF 7
DIOC 6	35	36	DIOF 6
DIOC 5	37	38	DIOF 5
DIOC 4	39	40	DIOF 4
DIOC 3	41	42	DIOF 3
DIOC 2	43	44	DIOF 2
DIOC 1	45	46	DIOF 1
DIOC 0	47	48	DIOF 0
+5V	49	50	Ground

1.3 Personality 0x21: 96 Digital I/O Lines (48 Buffered, 48 Non-buffered)

This personality provides a total of 96 digital I/O in the “ABC” arrangement compatible with the Onyx-MM family.

The digital I/O is accessed through four 82C55 interfaces. The first two 82C55 circuits provide 48 unbuffered digital I/O through J3. The second two 82C55 circuits provide 48 buffered digital I/O through J4.

J3:Unbuffered Digital I/O Header

DIOA 7	1	2	DIOD 7
DIOA 6	3	4	DIOD 6
DIOA 5	5	6	DIOD 5
DIOA 4	7	8	DIOD 4
DIOA 3	9	10	DIOD 3
DIOA 2	11	12	DIOD 2
DIOA 1	13	14	DIOD 1
DIOA 0	15	16	DIOD 0
DIOB 7	17	18	DIOE 7
DIOB 6	19	20	DIOE 6
DIOB 5	21	22	DIOE 5
DIOB 4	23	24	DIOE 4
DIOB 3	25	26	DIOE 3
DIOB 2	27	28	DIOE 2
DIOB 1	29	30	DIOE 1
DIOB 0	31	32	DIOE 0
DIOC 7	33	34	DIOF 7
DIOC 6	35	36	DIOF 6
DIOC 5	37	38	DIOF 5
DIOC 4	39	40	DIOF 4
DIOC 3	41	42	DIOF 3
DIOC 2	43	44	DIOF 2
DIOC 1	45	46	DIOF 1
DIOC 0	47	48	DIOF 0
+5V	49	50	Ground

J4: Buffered Digital I/O Header

DIOA 7	1	2	DIOD 7
DIOA 6	3	4	DIOD 6
DIOA 5	5	6	DIOD 5
DIOA 4	7	8	DIOD 4
DIOA 3	9	10	DIOD 3
DIOA 2	11	12	DIOD 2
DIOA 1	13	14	DIOD 1
DIOA 0	15	16	DIOD 0
DIOB 7	17	18	DIOE 7
DIOB 6	19	20	DIOE 6
DIOB 5	21	22	DIOE 5
DIOB 4	23	24	DIOE 4
DIOB 3	25	26	DIOE 3
DIOB 2	27	28	DIOE 2
DIOB 1	29	30	DIOE 1
DIOB 0	31	32	DIOE 0
DIOC 7	33	34	DIOF 7
DIOC 6	35	36	DIOF 6
DIOC 5	37	38	DIOF 5
DIOC 4	39	40	DIOF 4
DIOC 3	41	42	DIOF 3
DIOC 2	43	44	DIOF 2
DIOC 1	45	46	DIOF 1
DIOC 0	47	48	DIOF 0
+5V	49	50	Ground

2 REPROGRAMMING THE CONFIGURATION FLASH

Diamond Systems recommends the iMPACT JTAG tool, available free in the Xilinx ISE Webpack. The Webpack is currently available at this URL: http://www.xilinx.com/ise/logic_design_prod/webpack.htm

The end user will also require a suitable JTAG programming cable. This cable must connect to the target device using a 6-pin connector compatible with 3.3V logic:

VDD	1
GND	2
TCK	3
TDO	4
TDI	5
TMS	6

One suitable cable can be found here: http://www.nuhorizons.com/products/digilent/jtag-cable.html

Personalities provided by Diamond Systems will be available as .mcs files. These files can be used in the iMPACT software tool to directly reprogram the configuration flash.